Confirmation No.: 2656

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Docket No.: SON-223(80001-2230)

IN THE CALLED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yukio OKUDA

Application No.: 09/964,540 Art Unit: 2857

Filed: September 28, 2001 Examiner: Paul L. Kim

For: METHOD AND APPARATUS OF DETERMINING

DEFECT-FREE SEMICONDUCTOR INTEGRATED

CIRCUIT

REQUEST FOR INITIALED FORM PTO-1449 FILED WITH AN INFORMATION DISCLOSURE STATEMENT ON APRIL 1, 2002

MAIL STOP ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

A Notice of Allowance was issued by the PTO on January 15, 2004 in connection with the above-identified application. Upon our final review of the file, it was noted that the initialed Form PTO/SB/08A accompanying the Information Disclosure Statement filed on April 1, 2002 has not been returned. Applicant respectfully requests an initialed copy of the PTO/SB/08A form be sent as soon as possible to evidence consideration of the references by the Examiner. For the convenience of the Examiner, a copy of the IDS as filed is enclosed.

Dated: February 13, 2004

Respectfully submitted,

Ronald P. Kananen

Registration No.: 24,104

RADER, FISHMAN & GRAUER, PLLC

Lion Building 1233 20th Street, N.W., Suite 501 Washington, D.C. 20036

Tel: (202) 955-3750 Fax: (202) 955-3751 Customer No. 23353

Application: AUDA Docket No.: DN 230 Date Mailed: Title: UTD 100 AUDA Docket No.: DN 230 DN	Serial I		Filed: 09 3	28/01	Attorney:	RPK/tme
The following was/were received in the U.S. Patent & Trademark Office Mail Room on the date stamped hereon: Filing Form for Application* (in duplicate if charging deposit account).			Docket No.:			
Filing Form for Application* (in duplicate if charging deposit account). Originating in the U.S. National Phase under Chapter I National Phase under Chapter II Filing Form for Application* (in duplicate if charging deposit account). Provisional Application under 37 CFR 1.53(b) Request for Non-Publication R			<u> </u>			
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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of) `
Yukio OKUDA) Group Art No: 2858
Application No. 09/964,540) Examiner: Unknown
Filed: September 28, 2001))
For: METHOD AND APPARATUS OF DETERMINING DEFECT-FREE SEMICONDUCTOR INTEGRATED CIRCUIT))))
INFORMATION D	DISCLOSURE STATEMENT
Commissioner for Patents Washington, D.C. 20231	
Sir:	
directed to the information item(s) listed of indicated herein, one copy of each item(s) information be expressly considered during	ention of the Patent and Trademark Office is hereby in the attached PTO-1449. Unless otherwise is attached. It is respectfully requested that the ing the prosecution of this application, and that the ocear among the "References Cited" on any patent to
U.S. filing date or the date of filing a	Statement is being filed (a) within three months of the CPA, OR (b) before the mailing date of a first Office application, or (c) accompanies a Request for Continued is required.
Statement was cited in a cor	m of information contained in this Information Disclosure mmunication from a foreign patent office in a counterpart than three months prior to the filing of this Information FR §1.97(e)(1).
	Statement is being filed more than three months after ailing date of the first Office Action on the merits, but ejection or Notice of Allowance.
	each item of information contained in this Information ited in a communication from a foreign patent office in a

		counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
		b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
		c. Please charge our Deposit Account No. 18-0013 the \$180.00 fee and charge any additional fee or credit any overpayment to Deposit Account No. 18-0013 as needed to ensure consideration of the disclosed information.
	but ber Disclos payme credit	This Information Disclosure Statement is being filed more than three months after S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, fore payment of the Issue Fee. Applicant(s) hereby petition(s) that the Information sure Statement be considered. Attached is our check in the amount of \$180.00 in ent of the petition fee under 37 CFR §1.17(i)(1). Please charge any fee deficiency or any overpayment to Deposit Account No. 01-2300 as needed to ensure consideration disclosed information.
		a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
		b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
\boxtimes	4.	The reference(s) was/were discussed in the specification as filed.
	5. No. 1	In the event any fees are due with this paper, please charge our Deposit Account 8-0013.
1233 Suite	er, Fish 20 th S 501	Respectfully submitted, Ronald P. Kananen Registration No. 24,104 Treet, N.W.,
Tel:	(202)	, D.C. 20036 955-3750 955-3751
Lax.	(202)	000-0101

ATTY. DOCKET NO. U.S. DEPARTMENT OF COMMERCE **FORM PTO-1449** PATENT AND TRADEMARK OFFICE SON-2230 & TRADEMAS APPLICANT LIST OF REFERENCES CITED BY APPLICANT **OKUDA** FILING DATE

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

09/28/01

Sheet 1 of 1

SERIAL NO.

GROUP

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EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA	5,519,33	05/96	Righter			
	AB	5,889,408	03/99	Miller			
	AC	5,392,293	02/95	Hsue			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRA YES	NSLAT	ION ABST.
AD	2000-171529	06/00	JAPAN	<u></u>				х
AE	8-271584	10/96	JAPAN					Х
 AF	9-211088	08/97	JAPAN				<u>.</u>	X.
AG					<u> </u>	<u> </u>		<u> </u>

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)
T.W. Williams, R.H. Dennard, R. Kapur, M.R. Mercer, W. Maly, "Iddq Test: Sensitivity Analysis of Scaling," Int. Test Conf., pp. 786-792, Oct. 1996. AΗ A. Gattiker and W. Maly, "Toward Understanding: Iddq-Only Fails," Department of ECE, Carnegie Mellon University, Pittsburgh, PA. ΑI A. Keshavarzi, C. Hawkins, K. Roy, and V. De, "Effectiveness of Reverse Body Bias for Low Power CMOS Circuits," Microprocessor Research Labs, The University of New Mexico, and Purdue University, Intel Corporation, Hillsboro, OR 97124, USA ΑJ A. Keshavarzi, K. Roy, and C. Hawkin, "Intrinsic Leakage in LOw Power Deep Submicron CMOS Ics". ΑK

EXAMINER		DATE CONSIDERED		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.				